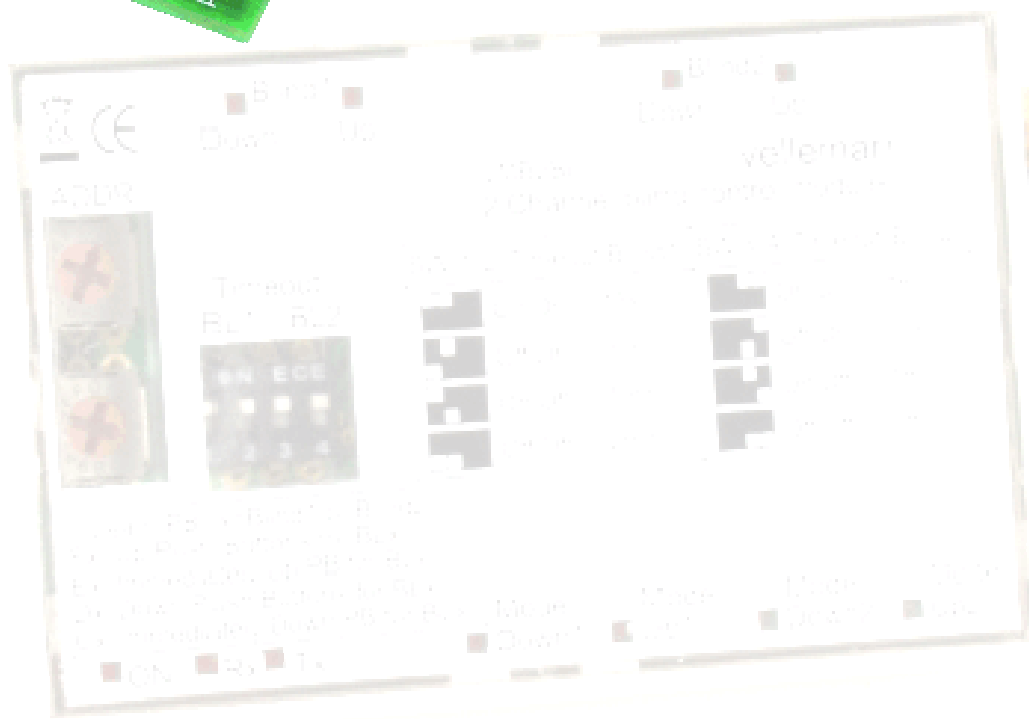


# VMB1USB



## USB interface PROTOCOL

## Data protocol

<STX><1-1-1-1-1-0-SID10-SID9><SID8...SID1><SID0-RTR-0-0-DLC3...0>  
 [<DATABYTE1>...<DATABYTE8>]<CHECKSUM><ETX>

Byte	Description
<STX>	start of transmission (H'0F')
<111110-SID10-SID9>	standard identifier bit 10 & 9
<SID8-SID1	standard identifier bit 8...1
<SID0-RTR-00-DLC3...DLC0>	SID0 = standard identifier bit 0 RTR = remote transmit request DLC3...DLC0 = number of data bytes
<DATABYTE1>...<DATABYTE8>	data
<CHECKSUM>	two's complement of sum of previous bytes
<ETX>	end of transmission (H'04')

### 'Bus off' message received:

<STX><H'F8'><H'00'><H'01'><COMMAND\_BUS\_OFF><CHECKSUM><ETX>  
 remark: COMMAND\_BUS\_OFF = H'09'

### 'Bus active' message received:

<STX><H'F8'><H'00'><H'01'><COMMAND\_BUS\_ACTIVE><CHECKSUM><ETX>  
 remark: COMMAND\_BUS\_OFF = H'0A'

### 'Receive buffer full' message received:

<STX><H'F8'><H'00'><H'01'><COMMAND\_RX\_BUFFER\_FULL><CHECKSUM><ETX>  
 remark: COMMAND\_RX\_BUFFER\_FULL = H'0B'

### 'Receive Ready' message received:

<STX><H'F8'><H'00'><H'01'><COMMAND\_RX\_READY><CHECKSUM><ETX>  
 remark: COMMAND\_RX\_READY = H'0C'

### 'Interface status request' send:

<STX><H'F8'><H'00'><H'01'><CMD\_INTERFACE\_STATUS\_REQUEST><CHECKSUM><ETX>  
 remark: CMD\_INTERFACE\_STATUS\_REQUEST = H'0E'

When the receive buffer of the interface module is full, no interface status will be returned.



## VELBUS protocol

Binary format:

<SOF-SID10...SID0-RTR-IDE-r0-DLC3...0-DATABYTE1...DATABYTE<sub>n</sub>-CRC15...CRC1-CRCDEL-ACK-ACKDEL-EOF7...EOF1-IFS3...IFS1>

<i>bits</i>	<i>Description</i>
SOF	Start Of Frame (always 0)
SID10 & SID9	Priority (00: highest ... 11: lowest priority)
SID8...SID1	Address
SID0	Always 0
RTR	Remote Transmit Request
IDE	Identifier Extension (always 0)
r0	reserved (always 0)
DLC3...DLC0	Data Length Code (0...8)
Databyte1	Command
Databyte2	Parameter
Databyte3	Parameter
Databyte4	Parameter
Databyte5	Parameter
Databyte6	Parameter
Databyte7	Parameter
Databyte8	Parameter
CRC15...CRC1	Cyclic Redundancy Checksum
CRCDEL	CRC Delimiter (always 1)
ACK	Acknowledge slot (transmit 1 readback 0 if received correctly)
ACKDEL	Acknowledge Delimiter (always 1)
EOF7...EOF1	End Of Frame (always 1111111)
IFS3...IFS1	InterFrame Space (always 111)